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EXAMINER	
VO, NGUYEN THANH	
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2618	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

**Office Action Summary**

Application No.

10/807,767

Applicant(s)

WALKER ET AL.

Examiner

Nguyen Vo

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-78 is/are pending in the application.
- 4a) Of the above claim(s) 75-78 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-74 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☒ Claim(s) 75-78 are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 27 December 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. ____.                                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date ____.  | 6) <input type="checkbox"/> Other: ____.                          |

**DETAILED ACTION**

***Election/Restrictions***

1. Applicant's election without traverse of Specie I in the reply filed on 12/22/2006 is acknowledged.

***Claim Objections***

2. Claims 77-78 are objected to because of the following informalities: the status of non-elected claims 77-78 should be changed to "(withdrawn)". Appropriate correction is required.

***Claim Rejections - 35 USC § 112***

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claims 2-3, 16, 46-57, 73 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

As to claim 16, it is indefinite because it depends from itself. In order to overcome this rejection, it is suggested that claim 16 should depend from claim 15.

As to claims 2-3, the recitation "the preamble **signal**" lacks clear antecedent basis.

As to claim 46, the recitation "the battery" lacks clear antecedent basis.

***Claim Rejections - 35 USC § 103***

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5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

7. Claims 1-3, 7, 12, 24, 26-27, 34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda (5,081,402, cited by examiner).

As to claim 1, Koleda discloses a wireless control system for operating a component at least in part in response to a wireless signal 14 from a remote control device 16 manipulable by a user (see the shade or window remote control system in figure 1), the wireless signal 14 including at least one preamble pulse 66 followed by a data command signal 14 (see figures 2-3, in this case the preamble pulse as claimed reads on the pulses 66; see also the command 14 at column 3 lines 15-17), the system

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comprising: at least one wireless receiver 18 associated with the component and configured for processing wireless signal 14, and at least one controller 26 associated with the component and controlling the receiver, the controller causing the receiver to be energized according to an energization paradigm selected from the group consisting of: energizing for a first energized period, then deenergizing for a short period if no preamble signal is detected, then energizing for a second energized period, and then deenergizing, at least if no preamble signal is detected, for period longer than the short period, prior to reenergizing the receiver; energizing for a first time period and then energizing for a second time period after a rest period the length of which ensures detection in at least one of the time periods of a preamble pulse if a preamble has been generated; and energizing the receiver once sometime within a period of a preamble pulse (see column 4 line 17 to column 5 line 10 which clearly discloses energizing the receiver once sometime within a period of a preamble pulse 66).

Koleda fails to disclose that the wireless remote control system in figure 1 is a radio-frequency (RF) control system. Those skilled in the art, however, would have recognized that the wireless link 14 in Koleda could also be a RF link without changing the scope and spirit of Koleda's invention. In addition, the examiner takes Official Notice that using a RF link in a remote control system is known in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the wireless link 14 in Koleda such that it is a RF link, in order to increase the controlling range for the shade or window control system in Koleda.

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As to claims 2-3, 26-27, Koleda fails to disclose that the plural pulses 66 has a duty cycle in excess of 50% as in claims 2, 26, and include at least six pulses as in claims 3, 27. Those skilled in the art, however, would have recognized that the above claimed limitations would not render the claims patentable over Koleda because they would merely depend on the structure of the preamble alone. Other conventional preamble structures could also be in used Koleda without changing the scope and spirit of Koleda's invention. In addition, the examiner takes Official Notice that a preamble signal which has a duty cycle in excess of 50%, and include at least six pulses is known in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the preamble pulse 66 in Koleda as claimed, in order to allow the receiver 18 enough time to detect the preamble pulses 66.

As to claim 7, Koleda further discloses at least one bypass capacitor 32 (see figure 1; column 4 lines 1-24). Koleda, however, fails to disclose that the capacitor 32 has a capacitance of below 500pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

As to claims 12, 34, see the motor 20 in figure 1 in Koleda. See also column 2 lines 56-61.

As to claim 24, it is rejected for similar reasons as set forth in claim 7.

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8. Claims 4, 28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Langlais (US 2003/0215032 A1, cited by examiner).

As to claims 4, 28, Koleda fails to disclose a fast filer and a slow filter as claimed. Langlais discloses disclose a fast filer 406 and a slow filter 408 (see figure 4) each electrically interposed between a receiver (232, 245. See figure 3)) and a controller (360, 368). See also paragraphs [0037]-[0038]. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Langlais to Koleda, in order to properly detect presence of a signal (as suggested by Langlais at paragraphs [0037]-[0038]).

9. Claims 5-6, 13-16, 18, 23, 29-30 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu (US 6,735,454 B1, cited by examiner).

As to claims 5, 29, Koleda fails to disclose operating at a low frequency clock during at least most of the long period and at a high clock frequency at least when the receiver is energized as claimed. Yu discloses operating at a low frequency clock during at least most of the long period and at a high clock frequency at least when the receiver is energized (see column 5 line 29 to column 6 line 18). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Yu to Koleda, in order to further reduce the power consumption of the battery (as suggested by Yu at column 5 lines 45-51).

As to claims 6, 14, 30, the combination of Koleda and Yu further discloses the claimed limitation (see Yu, column 5 line 64 to column 6 line 18).

As to claim 13, it is rejected for similar reasons as set forth in claim 5.

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As to claims 15-16, Koleda as modified by Yu fails to disclose that the plural pulses 66 has a duty cycle in excess of 50% as in claim 15, and include at least six pulses as in claim 16. Those skilled in the art, however, would have recognized that the above claimed limitations would not render the claims patentable over Koleda because they would merely depend on the structure of the preamble alone. Other conventional preamble structures could also be in used Koleda without changing the scope and spirit of Koleda's invention. In addition, the examiner takes Official Notice that a preamble signal which has a duty cycle in excess of 50%, and include at least six pulses is known in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the preamble pulse 66 in Koleda as claimed, in order to allow the receiver 18 enough time to detect the preamble pulses 66.

As to claim 18, Koleda further discloses at least one bypass capacitor 32 (see figure 1; column 4 lines 1-24). Koleda, however, fails to disclose that the capacitor 32 has a capacitance of below 500pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

As to claim 23, see the motor 20 in figure 1 in Koleda. See also column 2 lines 56-61.



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10. Claims 8, 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Pace (5,471,665, cited by examiner).

As to claims 8, 25, Koleda does disclose one bypass capacitor 32 (see figure 1), but fails to disclose plural bypass capacitors as claimed. Pace discloses a plural bypass capacitors (see column 4 lines 30-40). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Pace to Koleda, in order to obtain a much faster warm up time (as suggested by Pace at column 4 lines 34-38).

Still as to claims 8, 25, the combination of Koleda and Pace fails to disclose at least one capacitance not more than 100pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

11. Claims 9, 31, 35, 37-38, 42, 45 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Terreault (6,058,292, cited by examiner).

As to claims 9, 31, Koleda fails to disclose at least one SAW as claimed. Terreault discloses a SAW resonator circuit 60 for establishing an IF oscillator 24 for a receiver (see column 4 lines 5-14). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Terreault to Koleda, in order to increase the start up time.

As to claim 35, it is rejected for similar reasons as set forth in claim 9 above.

As to claims 37-38, Koleda fails to disclose that the plural pulses 66 has a duty cycle in excess of 50% as in claim 37, and include at least six pulses as in claim 38. Those skilled in the art, however, would have recognized that the above claimed limitations would not render the claims patentable over Koleda because they would merely depend on the structure of the preamble alone. Other conventional preamble structures could also be in used Koleda without changing the scope and spirit of Koleda's invention. In addition, the examiner takes Official Notice that a preamble signal which has a duty cycle in excess of 50%, and include at least six pulses is known in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the preamble pulse 66 in Koleda as claimed, in order to allow the receiver 18 enough time to detect the preamble pulses 66.

As to claim 42, Koleda further discloses at least one bypass capacitor 32 (see figure 1; column 4 lines 1-24). Koleda, however, fails to disclose that the capacitor 32 has a capacitance of below 500pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

As to claim 45, see the motor 20 in figure 1 in Koleda. See also column 2 lines 56-61.

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12. Claims 10, 32, 36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Terreault (6,058,292, cited by examiner) and Panther (5,537,676, cited by examiner).

As to claims 10, 32, 36, the combination of Koleda and Terreault fails to disclose an LC filter for filtering an IF signal as claimed. Panther discloses an LC filter 41 for filtering an IF signal (see column 3 lines 39-45). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Panther to the combination of Koleda and Terreault, in order to reduce the implementing cost of the receiver 18 (as suggested by Panther at column 3 lines 40-45).

13. Claims 11, 33, 46-48, 52, 56-57 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Tanaka (5,636,243, cited by examiner).

As to claims 11, 33, Koleda fails to disclose a DC-DC down converter as claimed. Tanaka discloses a DC-DC converter 318 electrically interposed between a battery 317 and receiver (see the receiver in figure 6) to provide voltage to the receiver (see column 4 lines 32-37). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Tanaka to Koleda, so that the receiver could be supplied with a desired voltage level (as suggested by Tanaka at column 4 lines 32-37).

As to claims 46, 56, they are rejected for similar reasons as set forth in claim 11 above.

As to claims 47-48, Koleda fails to disclose that the plural pulses 66 has a duty cycle in excess of 50% as in claim 47, and include at least six pulses as in claim 48.

Those skilled in the art, however, would have recognized that the above claimed limitations would not render the claims patentable over Koleda because they would merely depend on the structure of the preamble alone. Other conventional preamble structures could also be in used Koleda without changing the scope and spirit of Koleda's invention. In addition, the examiner takes Official Notice that a preamble signal which has a duty cycle in excess of 50%, and include at least six pulses is known in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the preamble pulse 66 in Koleda as claimed, in order to allow the receiver 18 enough time to detect the preamble pulses 66.

As to claim 52, Koleda further discloses at least one bypass capacitor 32 (see figure 1; column 4 lines 1-24). Koleda, however, fails to disclose that the capacitor 32 has a capacitance of below 500pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

As to claim 57, see the motor 20 in figure 1 in Koleda. See also column 2 lines 56-61.

14. Claim 17 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu and further in view of Langlais (US 2003/0215032 A1, cited by examiner).

As to claim 17, Koleda as modified by Yu fails to disclose a fast filter and a slow filter as claimed. Langlais discloses disclose a fast filter 406 and a slow filter 408 (see figure 4) each electrically interposed between a receiver (232, 245. See figure 3)) and a controller (360, 368). See also paragraphs [0037]-[0038]. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Langlais to Koleda, in order to properly detect presence of a signal (as suggested by Langlais at paragraphs [0037]-[0038]).

15. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu and further in view of Pace (5,471,665, cited by examiner).

As to claim 19, Koleda as modified by Yu does disclose one bypass capacitor 32 (see figure 1), but fails to disclose plural bypass capacitors as claimed. Pace discloses a plural bypass capacitors (see column 4 lines 30-40). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Pace to Koleda, in order to obtain a much faster warm up time (as suggested by Pace at column 4 lines 34-38).

Still as to claim 19, the combination of Koleda and Pace fails to disclose at least one capacitance not more than 100pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

16. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu and further in view of Terreault (6,058,292, cited by examiner).

As to claim 20, Koleda as modified by Yu fails to disclose at least one SAW as claimed. Terreault discloses a SAW resonator circuit 60 for establishing an IF oscillator 24 for a receiver (see column 4 lines 5-14). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Terreault to Koleda, in order to increase the start up time.

17. Claim 21 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu and Terreault (6,058,292, cited by examiner) and further in view of Panther (5,537,676, cited by examiner).

As to claim 21, the combination of Koleda and Yu and Terreault fails to disclose an LC filter for filtering an IF signal as claimed. Panther discloses an LC filter 41 for filtering an IF signal (see column 3 lines 39-45). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Panther to the combination of Koleda and Terreault, in order to reduce the implementing cost of the receiver 18 (as suggested by Panther at column 3 lines 40-45).

18. Claim 22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu and further in view of Tanaka (5,636,243, cited by examiner).

As to claim 22, Koleda as modified by Yu fails to disclose a DC-DC down converter as claimed. Tanaka discloses a DC-DC converter 318 electrically interposed between a battery 317 and receiver (see the receiver in figure 6) to provide voltage to the receiver (see column 4 lines 32-37). Therefore, it would have been obvious to one

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of ordinary skill in the art at the time of the invention to provide the above teaching of Tanaka to Koleda, so that the receiver could be supplied with a desired voltage level (as suggested by Tanaka at column 4 lines 32-37).

19. Claim 39 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Terreault and further in view of Langlais (US 2003/0215032 A1, cited by examiner).

As to claim 39, Koleda as modified by Terreault fails to disclose a fast filter and a slow filter as claimed. Langlais discloses disclose a fast filter 406 and a slow filter 408 (see figure 4) each electrically interposed between a receiver (232, 245. See figure 3)) and a controller (360, 368). See also paragraphs [0037]-[0038]. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Langlais to Koleda, in order to properly detect presence of a signal (as suggested by Langlais at paragraphs [0037]-[0038]).

20. Claims 40-41 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view Terreault and further in view of Yu (US 6,735,454 B1, cited by examiner).

As to claim 40, Koleda as modified by Terreault fails to disclose operating at a low frequency clock during at least most of the long period and at a high clock frequency at least when the receiver is energized as claimed. Yu discloses operating at a low frequency clock during at least most of the long period and at a high clock frequency at least when the receiver is energized (see column 5 line 29 to column 6 line 18). Therefore, it would have been obvious to one of ordinary skill in the art at the time

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of the invention to provide the above teaching of Yu to Koleda, in order to further reduce the power consumption of the battery (as suggested by Yu at column 5 lines 45-51).

As to claim 41, the combination of Koleda and Yu further discloses the claimed limitation (see Yu, column 5 line 64 to column 6 line 18).

21. Claim 43 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Terreault and further in view of Pace (5,471,665, cited by examiner).

As to claim 43, Koleda does disclose one bypass capacitor 32 (see figure 1), but fails to disclose plural bypass capacitors as claimed. Pace discloses a plural bypass capacitors (see column 4 lines 30-40). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Pace to Koleda, in order to obtain a much faster warm up time (as suggested by Pace at column 4 lines 34-38).

Still as to claim 43, the combination of Koleda, Terreault and Pace fails to disclose at least one capacitance not more than 100pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

22. Claim 44 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Terreault and further in view of Tanaka (5,636,243, cited by examiner).



As to claim 44, Koleda as modified by Terreault fails to disclose a DC-DC down converter as claimed. Tanaka discloses a DC-DC converter 318 electrically interposed between a battery 317 and receiver (see the receiver in figure 6) to provide voltage to the receiver (see column 4 lines 32-37). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Tanaka to Koleda, so that the receiver could be supplied with a desired voltage level (as suggested by Tanaka at column 4 lines 32-37).

23. Claim 49 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Tanaka and further in view of Langlais (US 2003/0215032 A1, cited by examiner).

As to claim 49, Koleda as modified by Tanaka fails to disclose a fast filter and a slow filter as claimed. Langlais discloses disclose a fast filter 406 and a slow filter 408 (see figure 4) each electrically interposed between a receiver (232, 245. See figure 3)) and a controller (360, 368). See also paragraphs [0037]-[0038]. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Langlais to Koleda, in order to properly detect presence of a signal (as suggested by Langlais at paragraphs [0037]-[0038]).

24. Claims 50-51 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view Tanaka and further in view of Yu (US 6,735,454 B1, cited by examiner).

As to claim 50, Koleda as modified by Tanaka fails to disclose operating at a low frequency clock during at least most of the long period and at a high clock frequency at least when the receiver is energized as claimed. Yu discloses operating at a low

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frequency clock during at least most of the long period and at a high clock frequency at least when the receiver is energized (see column 5 line 29 to column 6 line 18).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Yu to Koleda, in order to further reduce the power consumption of the battery (as suggested by Yu at column 5 lines 45-51).

As to claim 51, the combination of Koleda and Yu further discloses the claimed limitation (see Yu, column 5 line 64 to column 6 line 18).

25. Claim 53 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Tanaka and further in view of Pace (5,471,665, cited by examiner).

As to claim 53, Koleda as modified by Tanaka does disclose one bypass capacitor 32 (see figure 1), but fails to disclose plural bypass capacitors as claimed. Pace discloses a plural bypass capacitors (see column 4 lines 30-40). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Pace to Koleda, in order to obtain a much faster warm up time (as suggested by Pace at column 4 lines 34-38).

Still as to claim 53, the combination of Koleda and Pace fails to disclose at least one capacitance not more than 100pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

26. Claim 54 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Tanaka and further in view of Terreault (6,058,292, cited by examiner).

As to claim 54, Koleda as modified by Tanaka fails to disclose at least one SAW as claimed. Terreault discloses a SAW resonator circuit 60 for establishing an IF oscillator 24 for a receiver (see column 4 lines 5-14). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Terreault to Koleda, in order to increase the start up time.

27. Claim 55 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Tanaka and Terreault (6,058,292, cited by examiner) and further in view of Panther (5,537,676, cited by examiner).

As to claim 55, the combination of Koleda and Tanaka and Terreault fails to disclose an LC filter for filtering an IF signal as claimed. Panther discloses an LC filter 41 for filtering an IF signal (see column 3 lines 39-45). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Panther to the combination of Koleda and Terreault, in order to reduce the implementing cost of the receiver 18 (as suggested by Panther at column 3 lines 40-45).

28. Claims 58, 60-61, 63, 68, 69, 71 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Naidu (6,128,470, cited by examiner).

As to claims 58, 69, 71, first of all the rejection to claim 1 over Koleda as set forth above is herein incorporated. Koleda fails to disclose adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal as claimed. Naidu discloses adaptively adjusting a noise threshold above which

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a carrier must be detected to indicate the presence of a control signal (see column 5 lines 28-46). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Naidu to Koleda, in order to properly determine a presence of a control signal (as suggested by Naidu at column 5 lines 28-34).

As to claims 60-61, Koleda fails to disclose that the plural pulses 66 has a duty cycle in excess of 50% as in claim 60, and include at least six pulses as in claim 61. Those skilled in the art, however, would have recognized that the above claimed limitations would not render the claims patentable over Koleda because they would merely depend on the structure of the preamble alone. Other conventional preamble structures could also be in used Koleda without changing the scope and spirit of Koleda's invention. In addition, the examiner takes Official Notice that a preamble signal which has a duty cycle in excess of 50%, and include at least six pulses is known in the art. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the preamble pulse 66 in Koleda as claimed, in order to allow the receiver 18 enough time to detect the preamble pulses 66.

As to claim 63, Koleda further discloses at least one bypass capacitor 32 (see figure 1; column 4 lines 1-24). Koleda, however, fails to disclose that the capacitor 32 has a capacitance of below 500pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary

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skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

As to claim 68, see the motor 20 in figure 1 in Koleda. See also column 2 lines 56-61.

29. Claim 59 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view Naidu and further in view of Yu (US 6,735,454 B1, cited by examiner).

As to claim 59, Koleda as modified by Naidu fails to disclose operating at an intermediate frequency between a low frequency clock and a high frequency clock as claimed. Yu discloses operating at an intermediate frequency between a low frequency clock and a high frequency clock (see column 5 line 29 to column 6 line 18). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Yu to Koleda, in order to further reduce the power consumption of the battery (as suggested by Yu at column 5 lines 45-51).

30. Claim 62 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Naidu and further in view of Langlais (US 2003/0215032 A1, cited by examiner).

As to claim 62, Koleda as modified by Naidu fails to disclose a fast filter and a slow filter as claimed. Langlais discloses disclose a fast filter 406 and a slow filter 408 (see figure 4) each electrically interposed between a receiver (232, 245. See figure 3)) and a controller (360, 368). See also paragraphs [0037]-[0038]. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to

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provide the above teaching of Langlais to Koleda, in order to properly detect presence of a signal (as suggested by Langlais at paragraphs [0037]-[0038]).

31. Claim 64 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Naidu and further in view of Pace (5,471,665, cited by examiner).

As to claim 64, Koleda does disclose one bypass capacitor 32 (see figure 1), but fails to disclose plural bypass capacitors as claimed. Pace discloses a plural bypass capacitors (see column 4 lines 30-40). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Pace to Koleda, in order to obtain a much faster warm up time (as suggested by Pace at column 4 lines 34-38).

Still as to claim 64, the combination of Koleda and Pace fails to disclose at least one capacitance not more than 100pF as claimed. Those skilled in the art, however, would have recognized that the above claimed limitation would not render the claim patentable over Koleda because it would merely depend on how fast the settling time is (see Koleda, column 4 lines 8-15). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the capacitor 32 in Koleda as claimed, in order to minimize the settling time of the receiver.

32. Claim 65 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Naidu and further in view of Terreault (6,058,292, cited by examiner).

As to claim 65, Koleda as modified by Naidu fails to disclose at least one SAW as claimed. Terreault discloses a SAW resonator circuit 60 for establishing an IF oscillator 24 for a receiver (see column 4 lines 5-14). Therefore, it would have been obvious to

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one of ordinary skill in the art at the time of the invention to provide the above teaching of Terreault to Koleda, in order to increase the start up time.

33. Claim 66 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Naidu and Terreault (6,058,292, cited by examiner) and further in view of Panther (5,537,676, cited by examiner).

As to claim 66, the combination of Koleda and Naidu and Terreault fails to disclose an LC filter for filtering an IF signal as claimed. Panther discloses an LC filter 41 for filtering an IF signal (see column 3 lines 39-45). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Panther to the combination of Koleda and Naidu and Terreault, in order to reduce the implementing cost of the receiver 18 (as suggested by Panther at column 3 lines 40-45).

34. Claim 67 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Naidu and further in view of Tanaka (5,636,243, cited by examiner).

As to claim 67, Koleda as modified by Naidu fails to disclose a DC-DC down converter as claimed. Tanaka discloses a DC-DC converter 318 electrically interposed between a battery 317 and receiver (see the receiver in figure 6) to provide voltage to the receiver (see column 4 lines 32-37). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Tanaka to Koleda, so that the receiver could be supplied with a desired voltage level (as suggested by Tanaka at column 4 lines 32-37).

35. Claim 70 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Yu and further in view of Naidu (6,128,470, cited by examiner).

As to claim 70, first of all the rejection to claim 1 over Koleda as set forth above is herein incorporated. Koleda fails to disclose adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal as claimed. Naidu discloses adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal (see column 5 lines 28-46). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Naidu to Koleda, in order to properly determine a presence of a control signal (as suggested by Naidu at column 5 lines 28-34).

36. Claim 72 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Terreault and further in view of Naidu (6,128,470, cited by examiner).

As to claim 72, first of all the rejection to claim 1 over Koleda as set forth above is herein incorporated. Koleda fails to disclose adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal as claimed. Naidu discloses adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal (see column 5 lines 28-46). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Naidu to Koleda, in order to properly determine a presence of a control signal (as suggested by Naidu at column 5 lines 28-34).



37. Claim 73 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Tanaka and further in view of Naidu (6,128,470, cited by examiner).

As to claim 73, first of all the rejection to claim 1 over Koleda as set forth above is herein incorporated. Koleda fails to disclose adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal as claimed. Naidu discloses adaptively adjusting a noise threshold above which a carrier must be detected to indicate the presence of a control signal (see column 5 lines 28-46). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Naidu to Koleda, in order to properly determine a presence of a control signal (as suggested by Naidu at column 5 lines 28-34).

38. Claim 74 is rejected under 35 U.S.C. 103(a) as being unpatentable over Koleda in view of Hunzinger (6,829,493, cited by examiner).

As to claim 74, first of all the rejection to claim 1 over Koleda as set forth above is herein incorporated. Koleda fails to disclose energizing for a first energized period, then deenergizing for a short period if no preamble signal is detected, then energizing for a second energized period, and then deenergizing, at least if no preamble signal is detected, for period longer than the short period, prior to reenergizing the receiver. Such teaching is taught by Hunzinger (see column 1 lines 61-65). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide the above teaching of Hunzinger to Koleda, in order to further reduce the battery consumption (as suggested by Hunzinger).

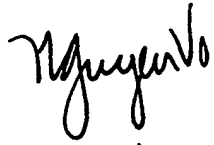
**Conclusion**

39. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nguyen Vo whose telephone number is (571) 272-7901. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban can be reached on (571) 272-7899. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nguyen Vo  
Primary Examiner  
Art Unit 2618

  
3/18/2007